

9-100
FORM 1082

FORM PTO-1082

File Number: MTS-3206US

THE ASSISTANT COMMISSIONER FOR PATENTS
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the patent application of

Inventor: Yasuhiro Mori and Masaki Yamauchi

For: DATA INPUT APPARATUS, DATA INPUT SYSTEM, DISPLAYED DATA ANALYZING APPARATUS
AND MEDIUM

Enclosed are:

- ☒ 33 sheets of drawing.
- ☒ An assignment of the invention to Matsushita Electric Industrial Co., Ltd.
- ☒ A Declaration and Power of Attorney.
- ☐ A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.2
- ☒ Claim to Right of Priority w/certified copy of Japanese application no(s). 11-256330.
- ☐ This application claims the benefit of U.S. Provisional Application No. _____ filed _____.

The filing fee has been calculated as shown below:

	(Col. 1)	(Col. 2)
FOR:	No. Filed	No. Extra
Basic Fee		
Total Claims	24-20=	4*
Indep Claims	3-3=	0*
<input type="checkbox"/> Multiple Dependent Claim Presented		

*If the difference in Col. 1 is less than zero,
enter "0" in Col. 2.

SMALL ENTITY

Rate	Fee
	\$345
x \$9	\$
x \$39	\$
+ \$130	\$
TOTAL	\$

OTHER THAN A
SMALL ENTITY

OR	Rate	Fee
OR		\$ 690
OR	x \$18	\$ 72
OR	x \$78	\$
OR	+ \$260	\$
OR	TOTAL	\$ 762

- ☐ Please charge my Deposit Account No. **18-0350** the amount of \$. A duplicate copy of this sheet is enclosed.
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Respectfully submitted,

[Signature]
Allan Ratner, Reg.No.19,717

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Date of Deposit: September 6, 2000

I hereby certify that this paper and fee are being deposited, under 37 C.F.R. § 1.10 and with sufficient postage, using the "Express Mail Post Office to Addressee" service of the United States Postal Service on the date indicated above and that the deposit is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

[Signature]

Kathleen Libby

JC853 U.S. PTO
09/06/00
09/656316

Fig. 1

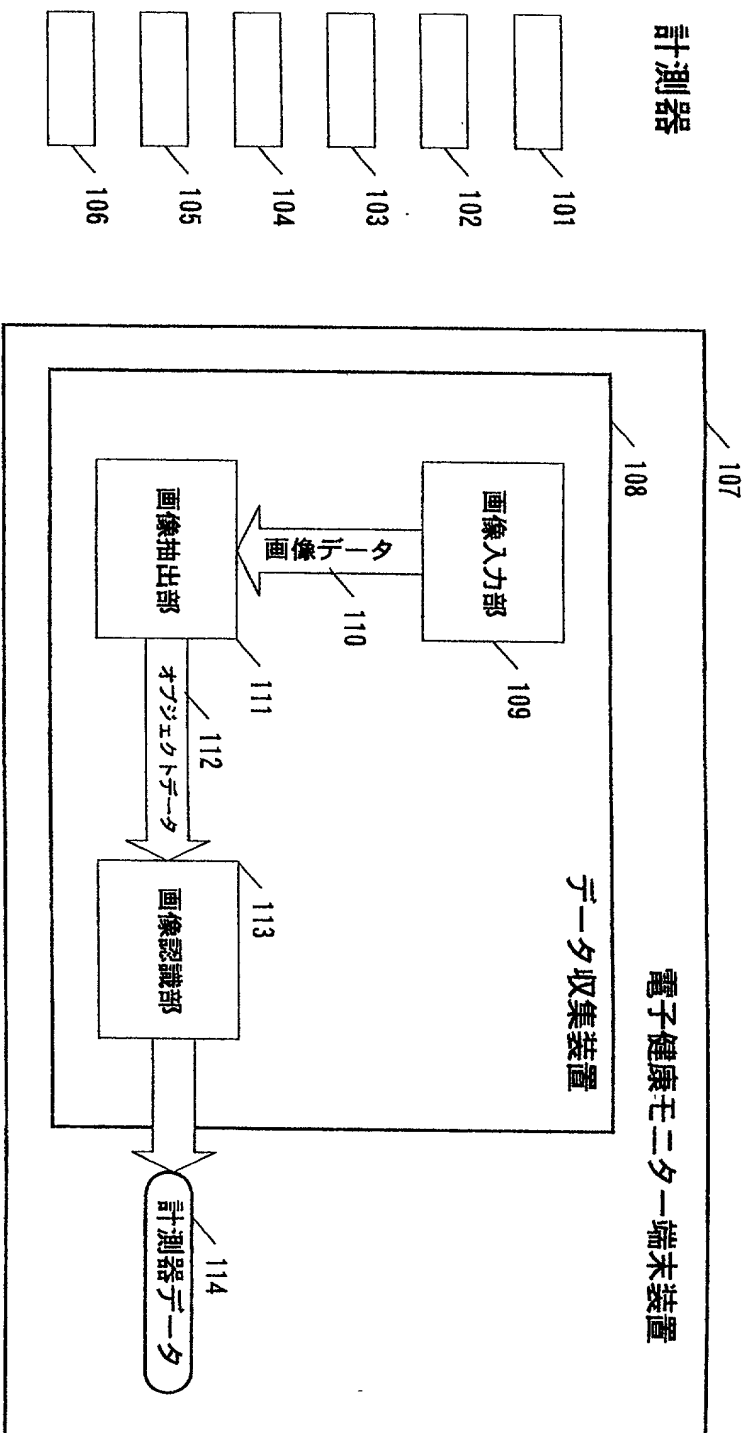


Fig. 2

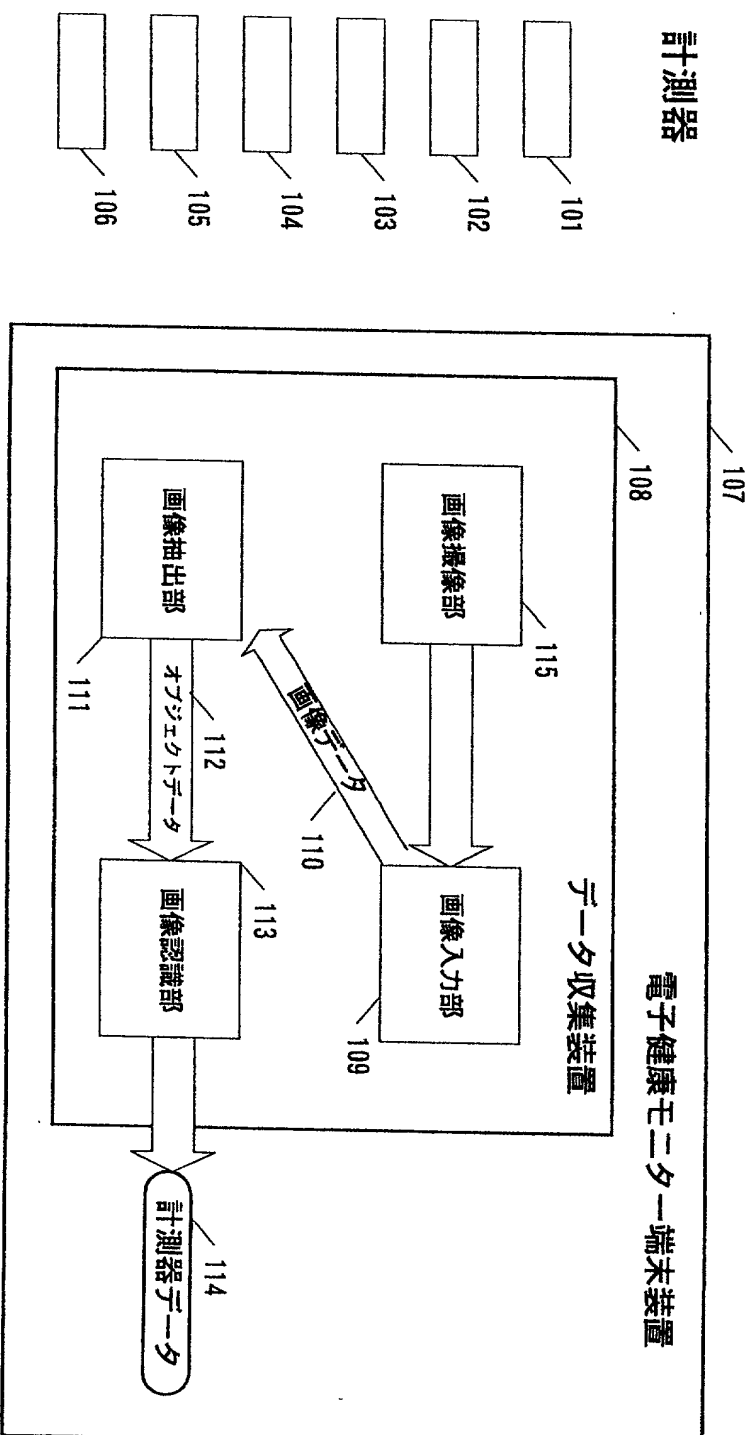


Fig. 3

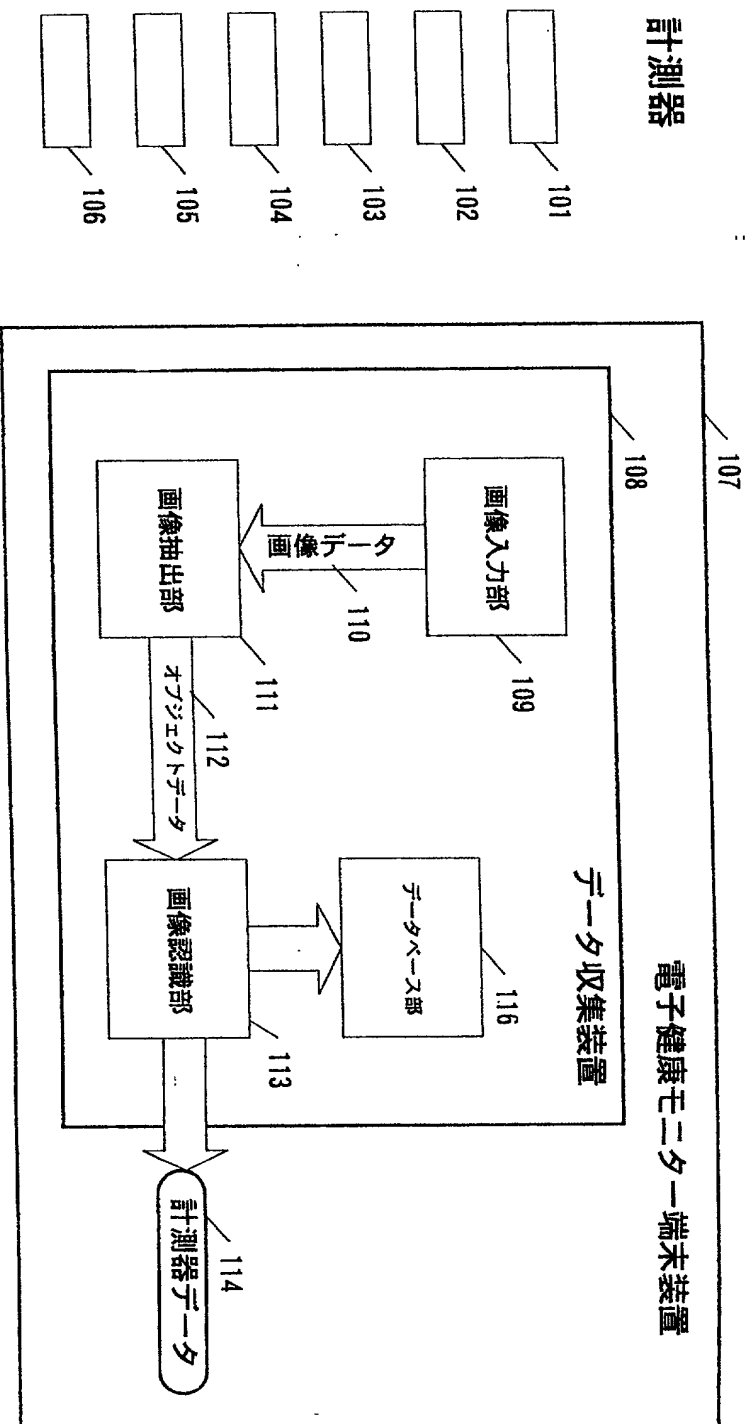


Fig. 5

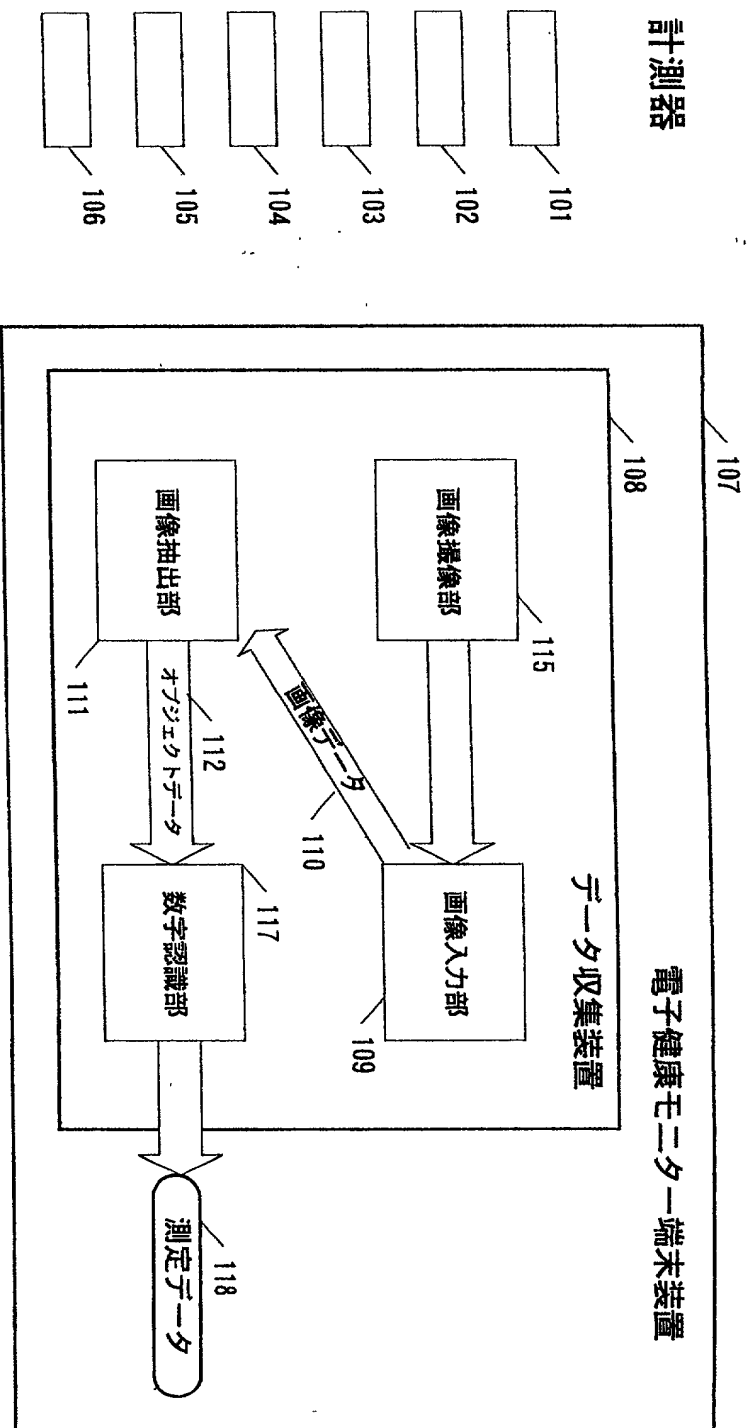
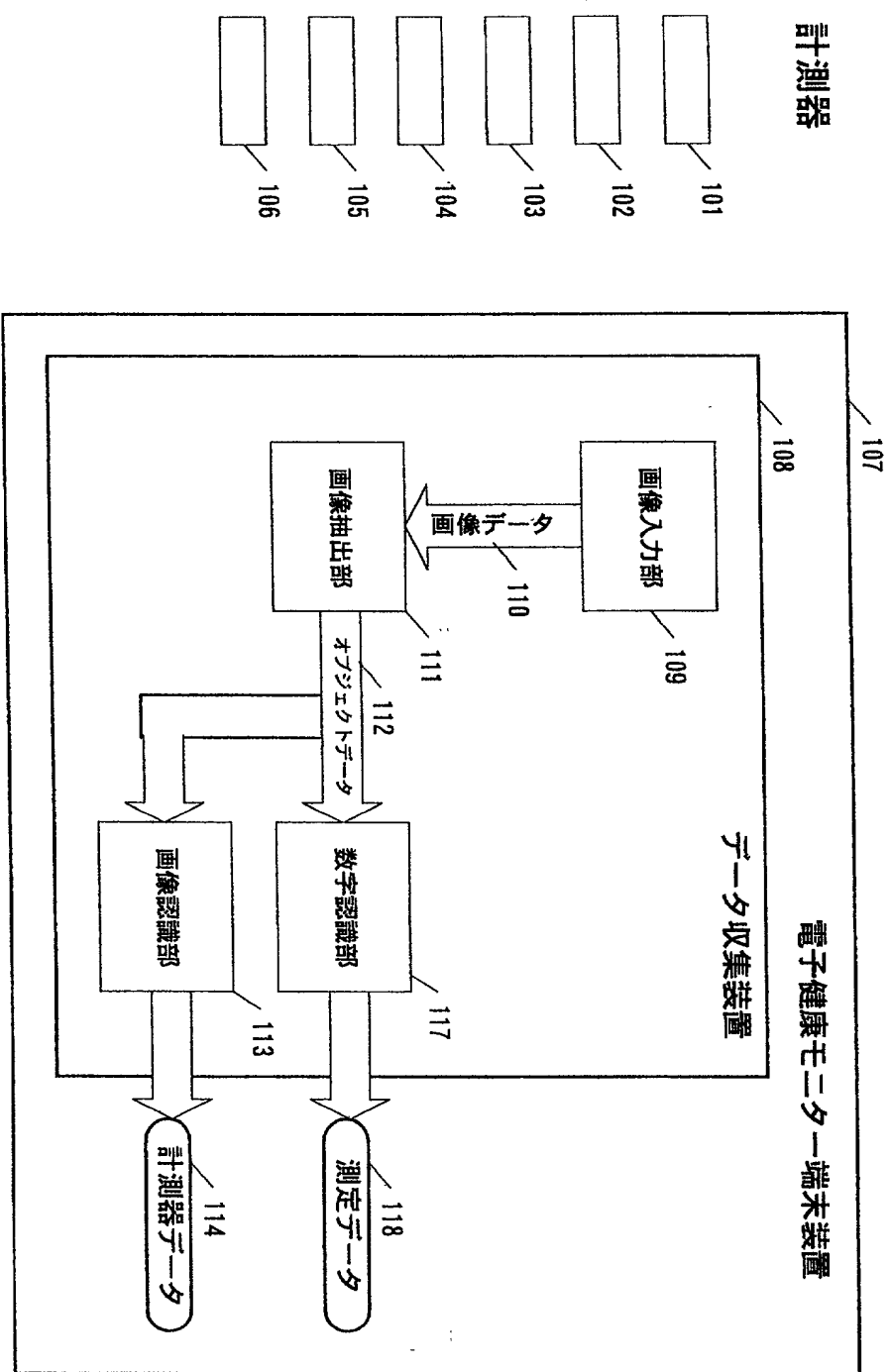


Fig. 6



Fi 8. 7

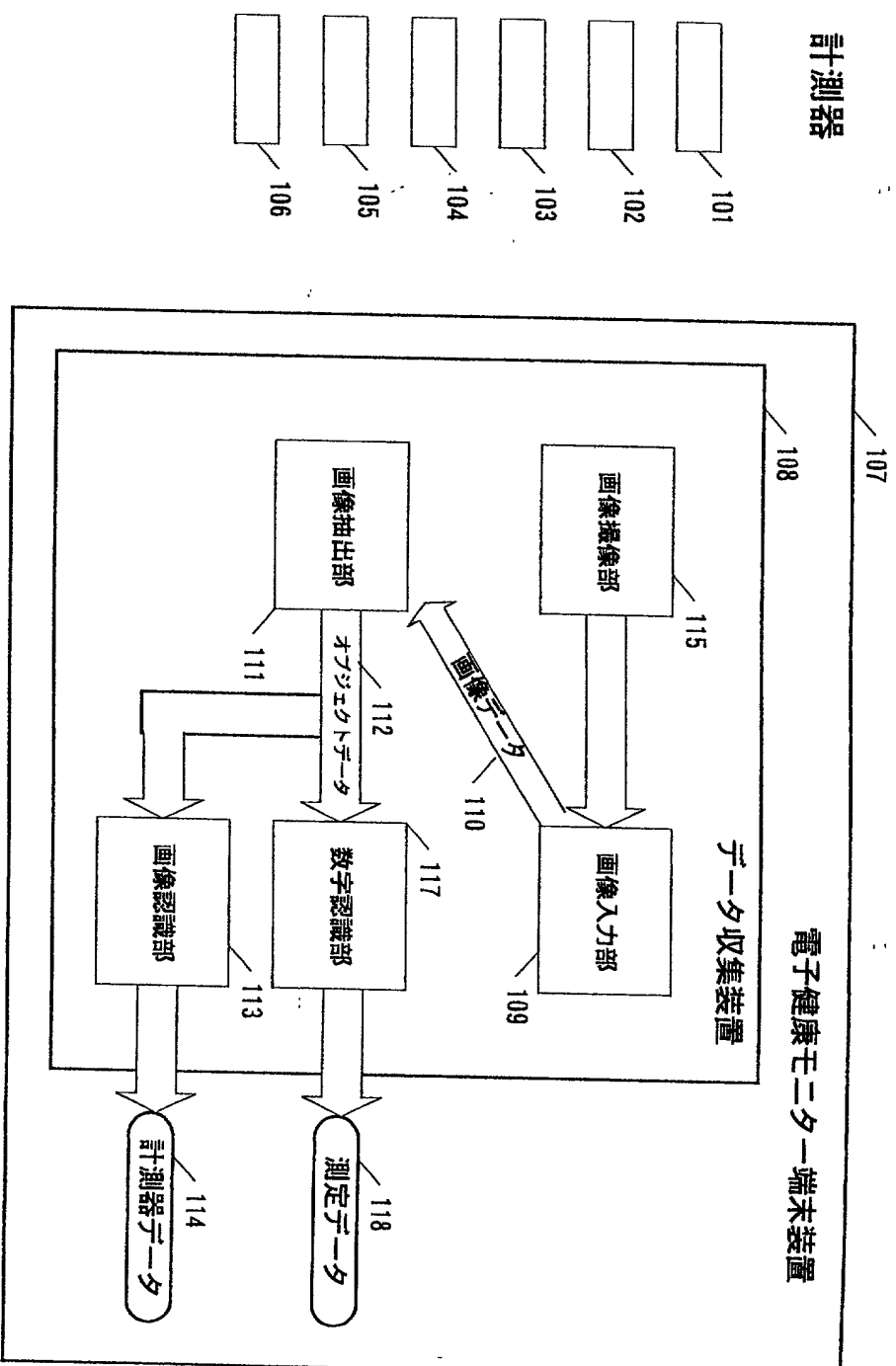


Fig. 8

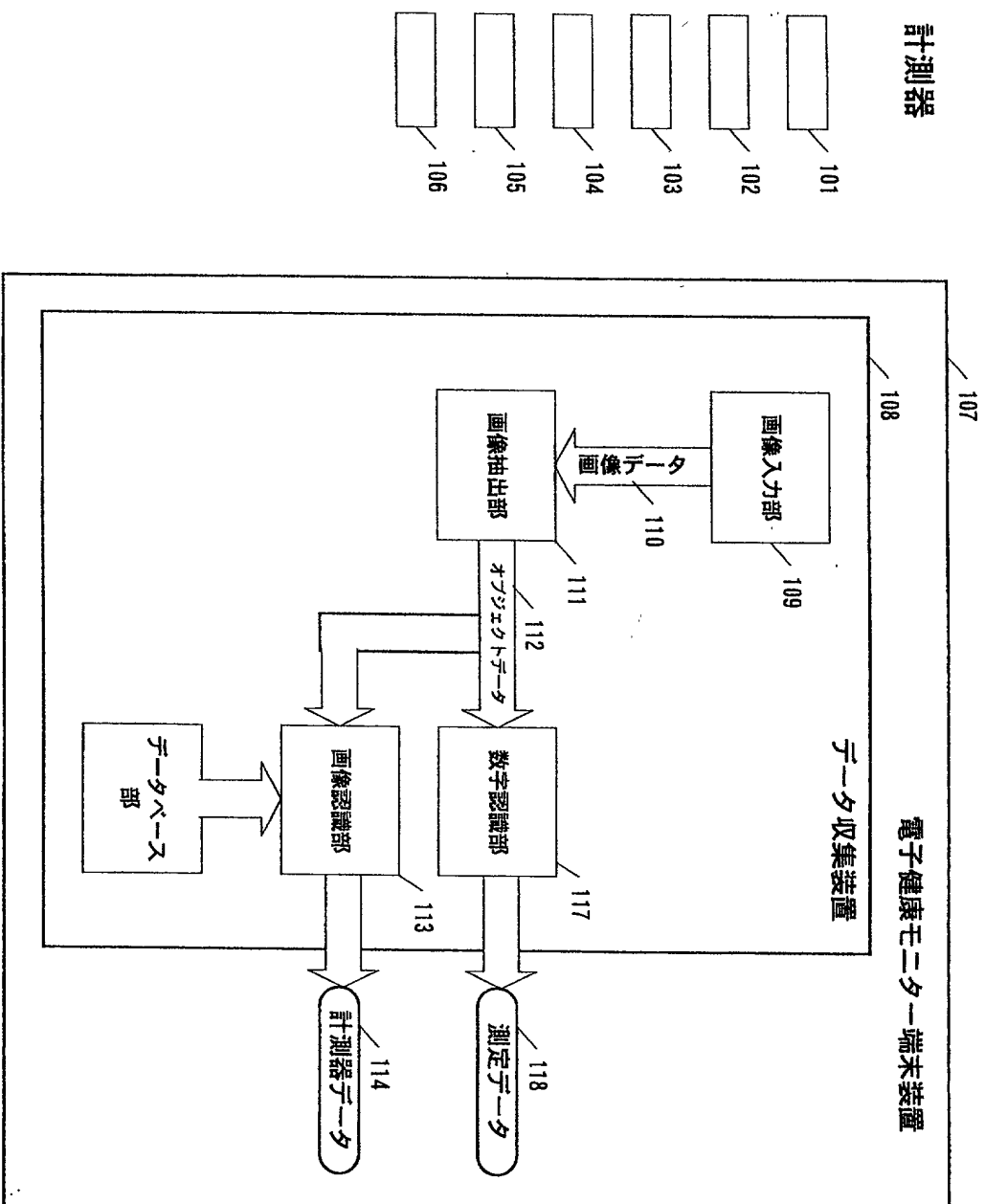


Fig. 9

アナログ
計測器

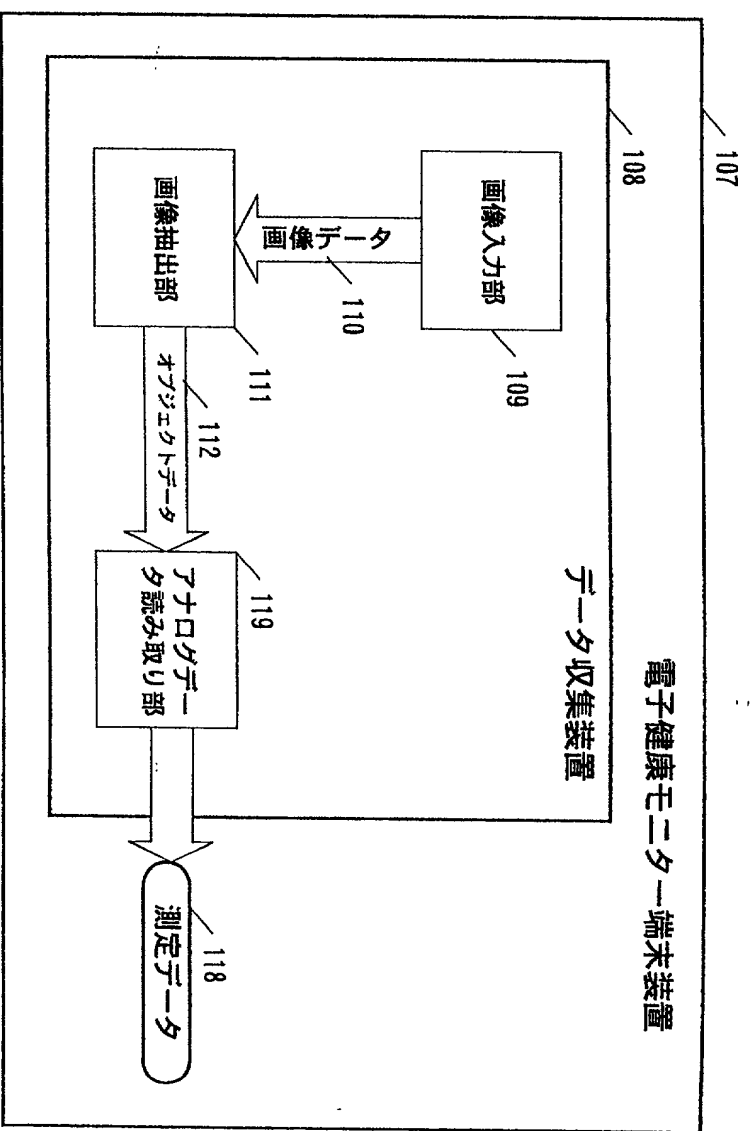
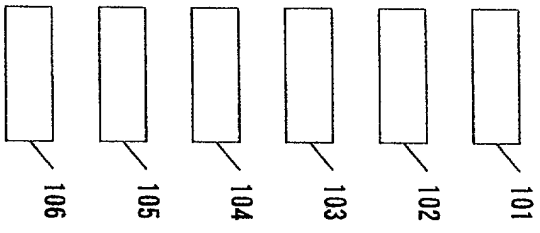


Fig. 10

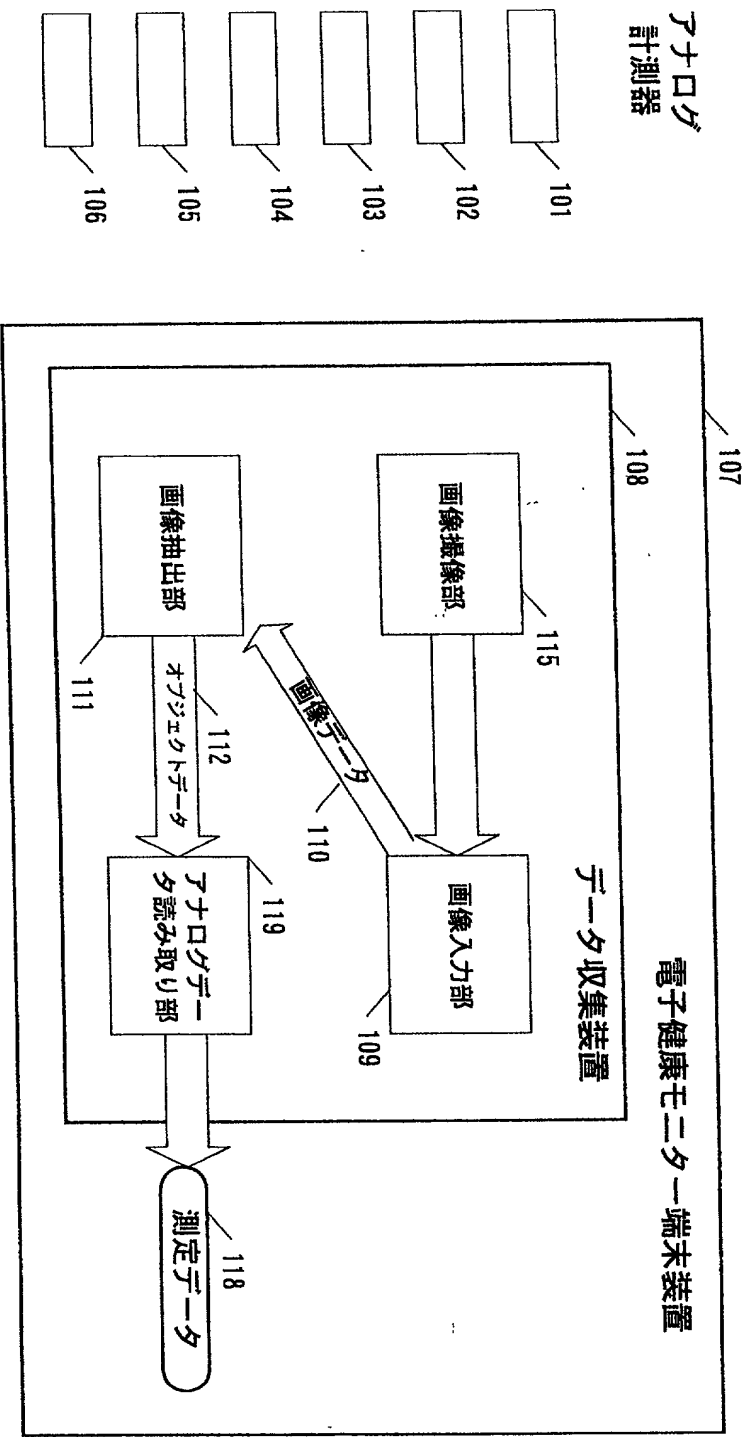


FIG. 11

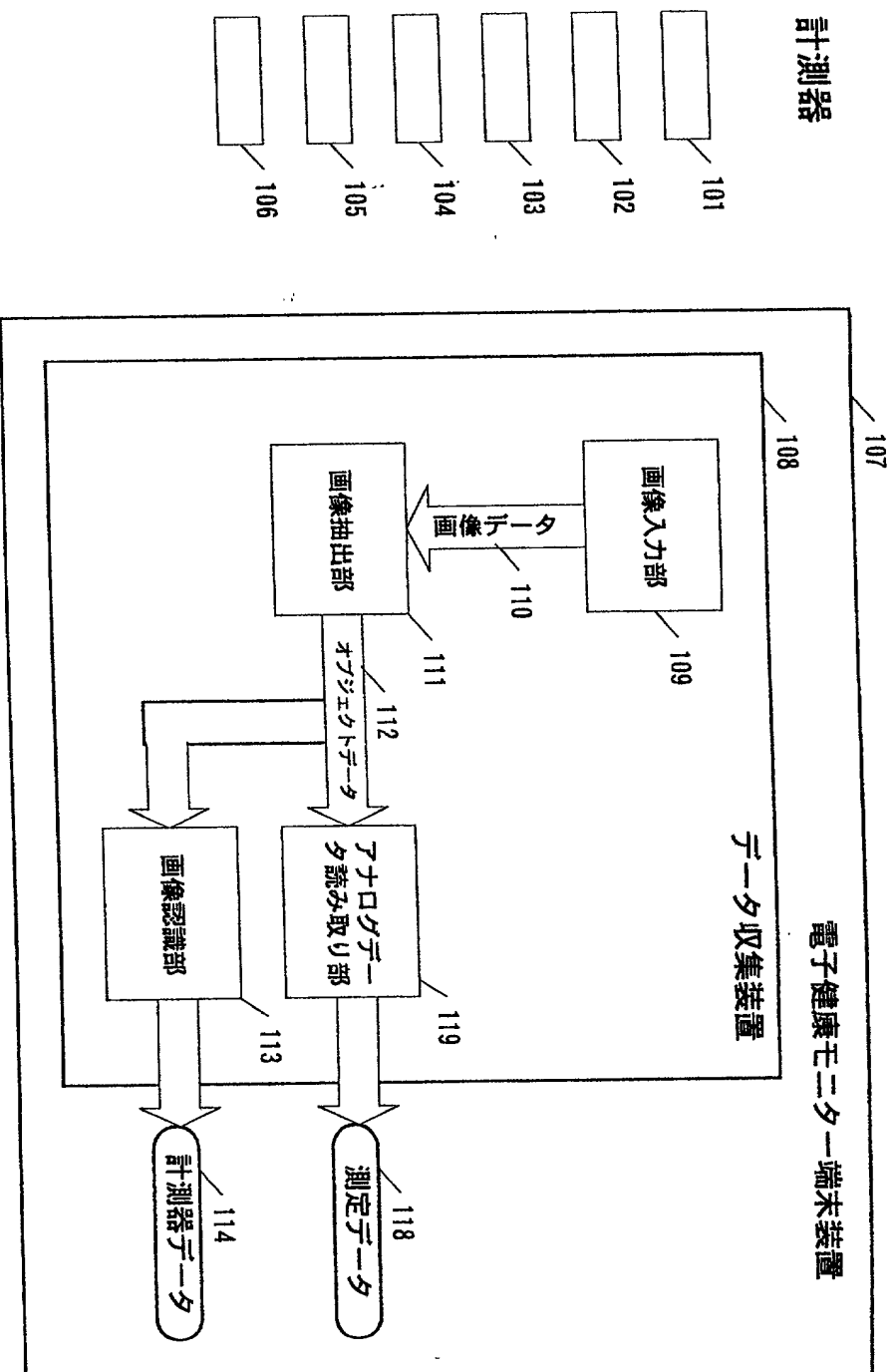


Fig. 12

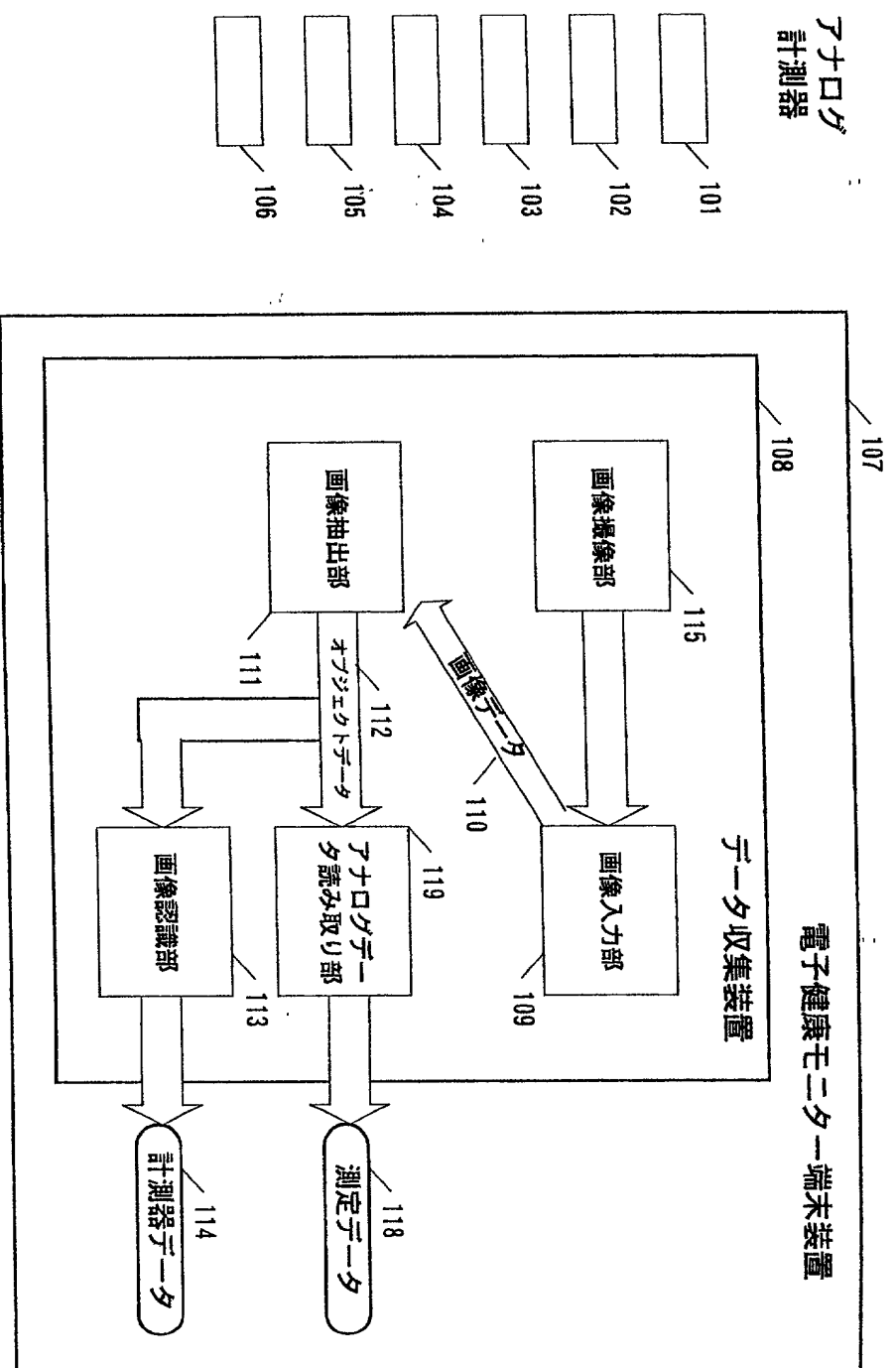


Fig. 13

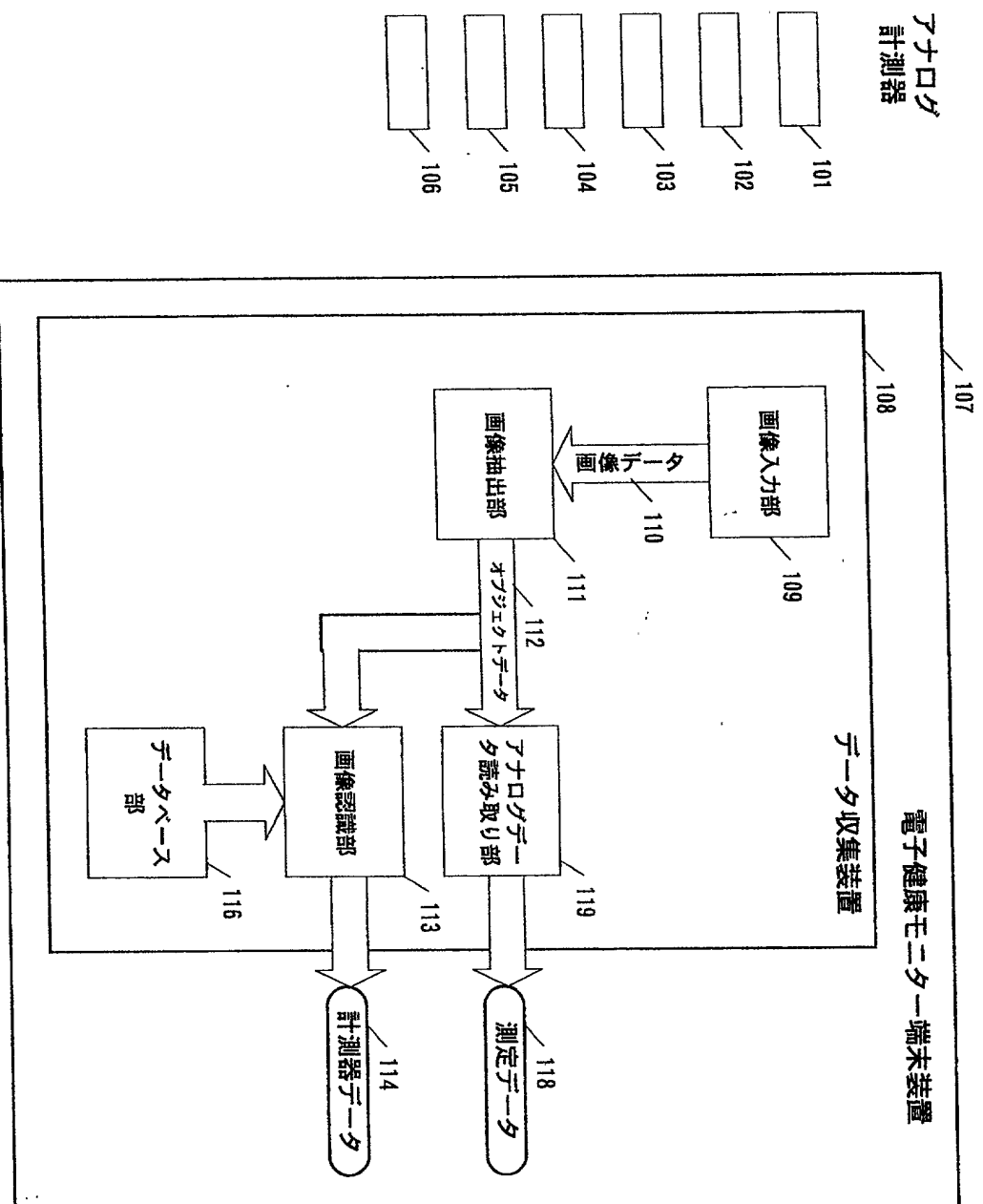
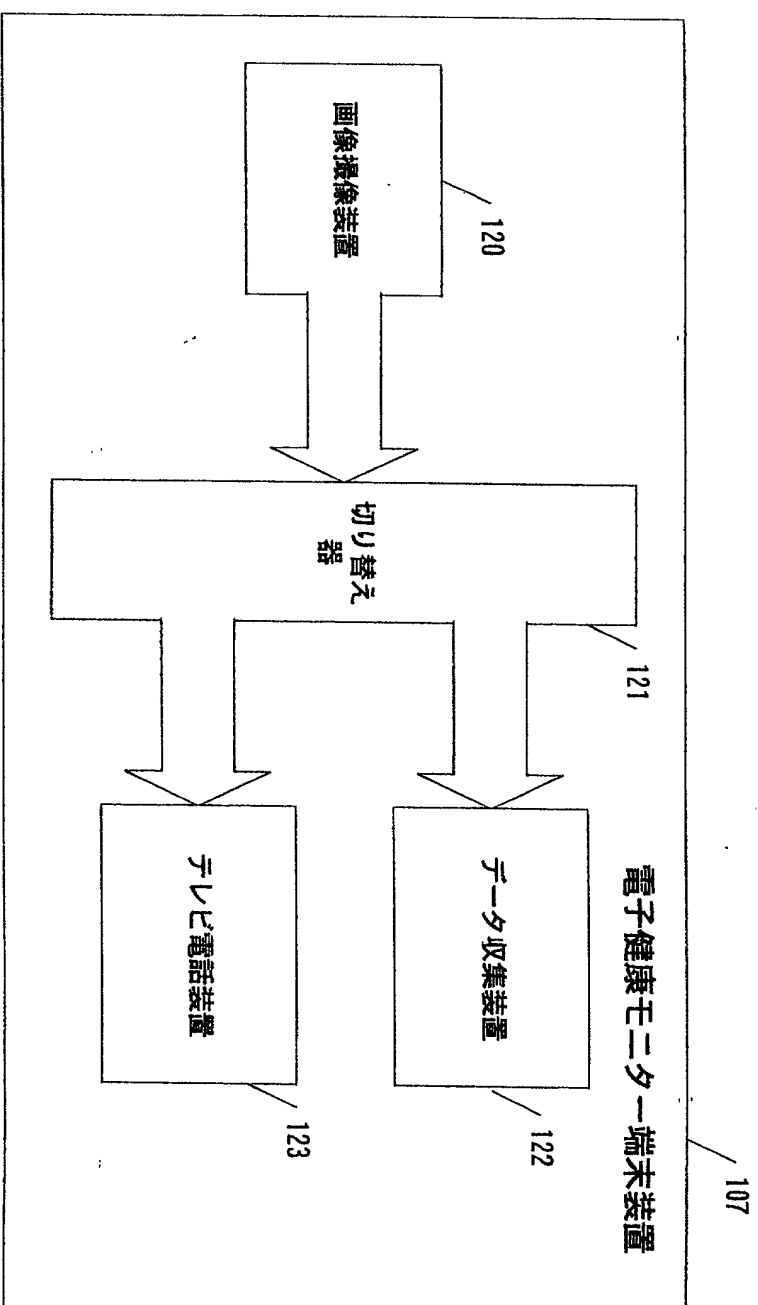


Fig. 14.



Fi. 8. 1

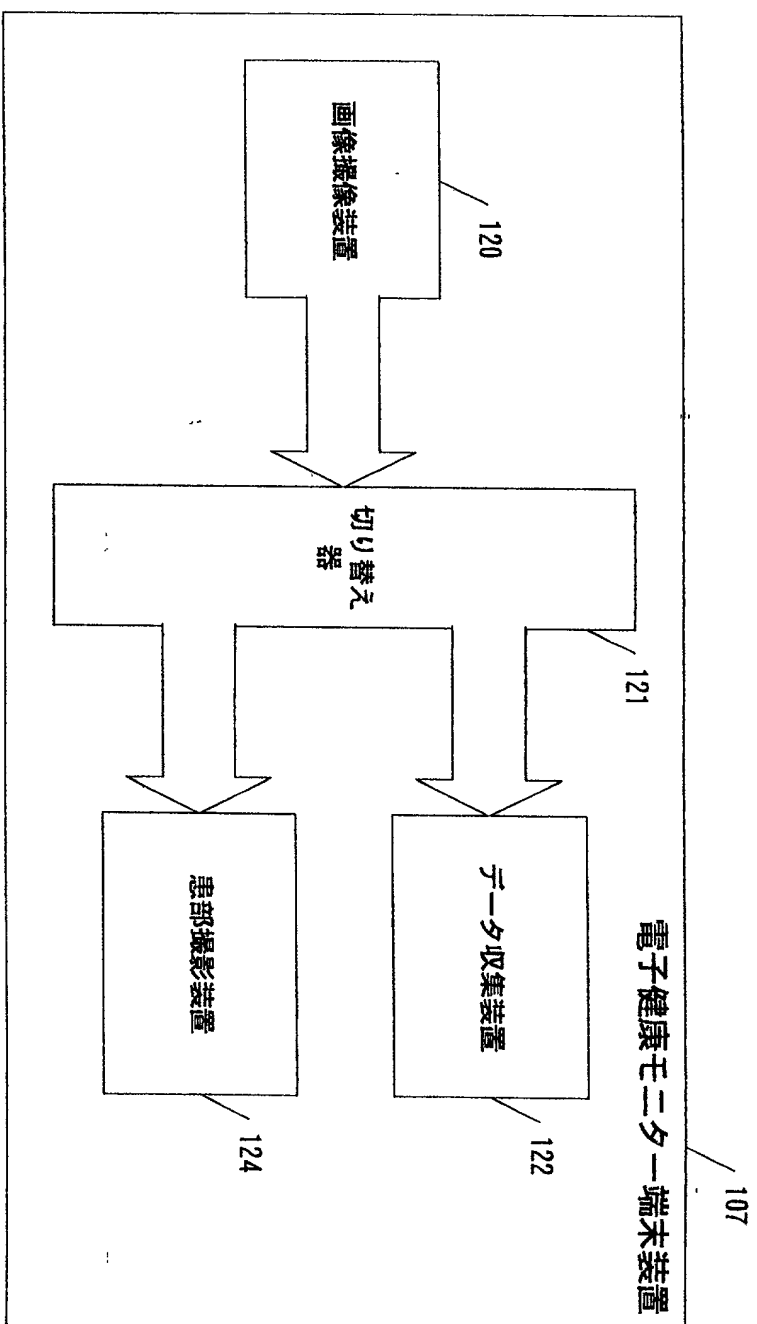


Fig. 16

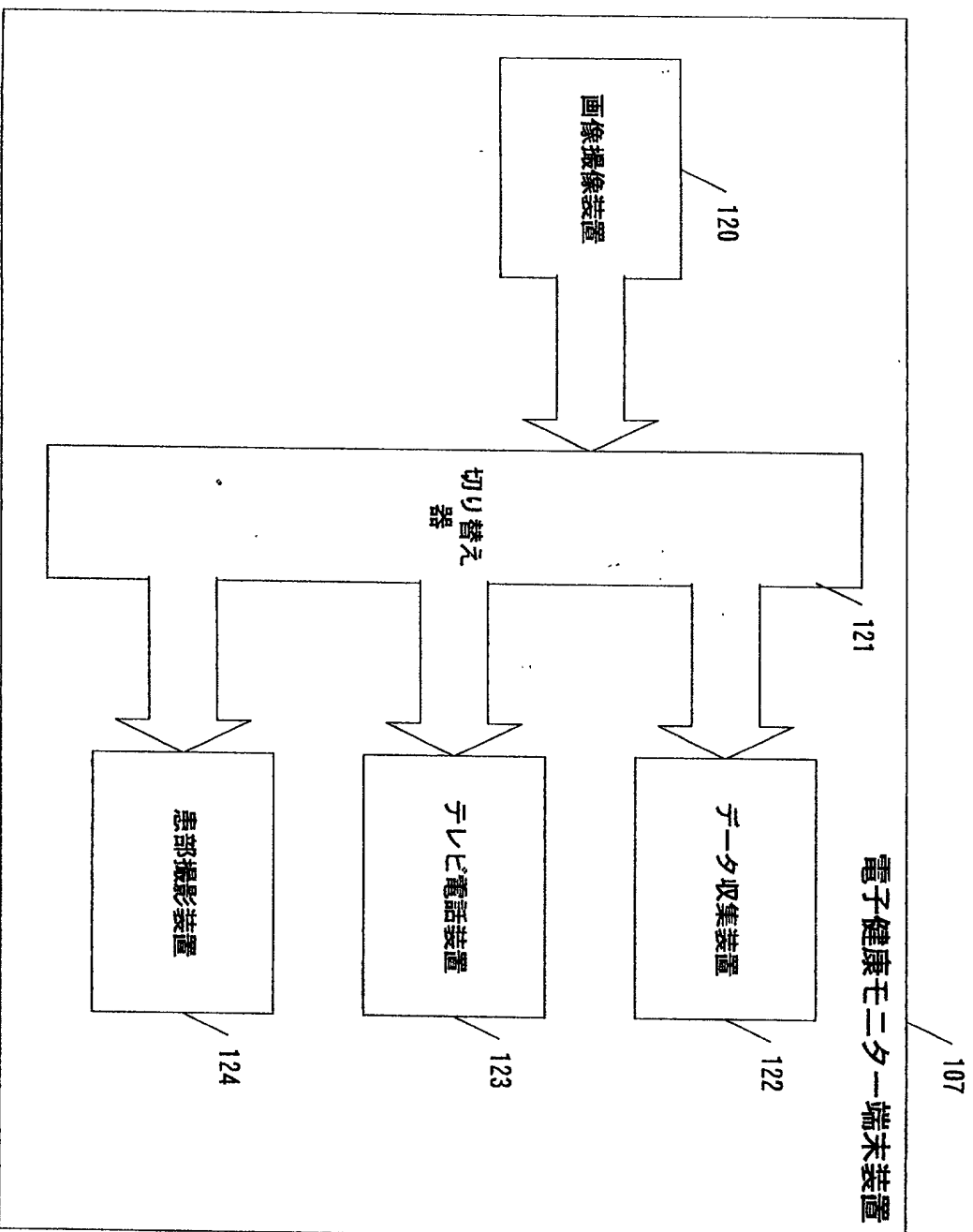
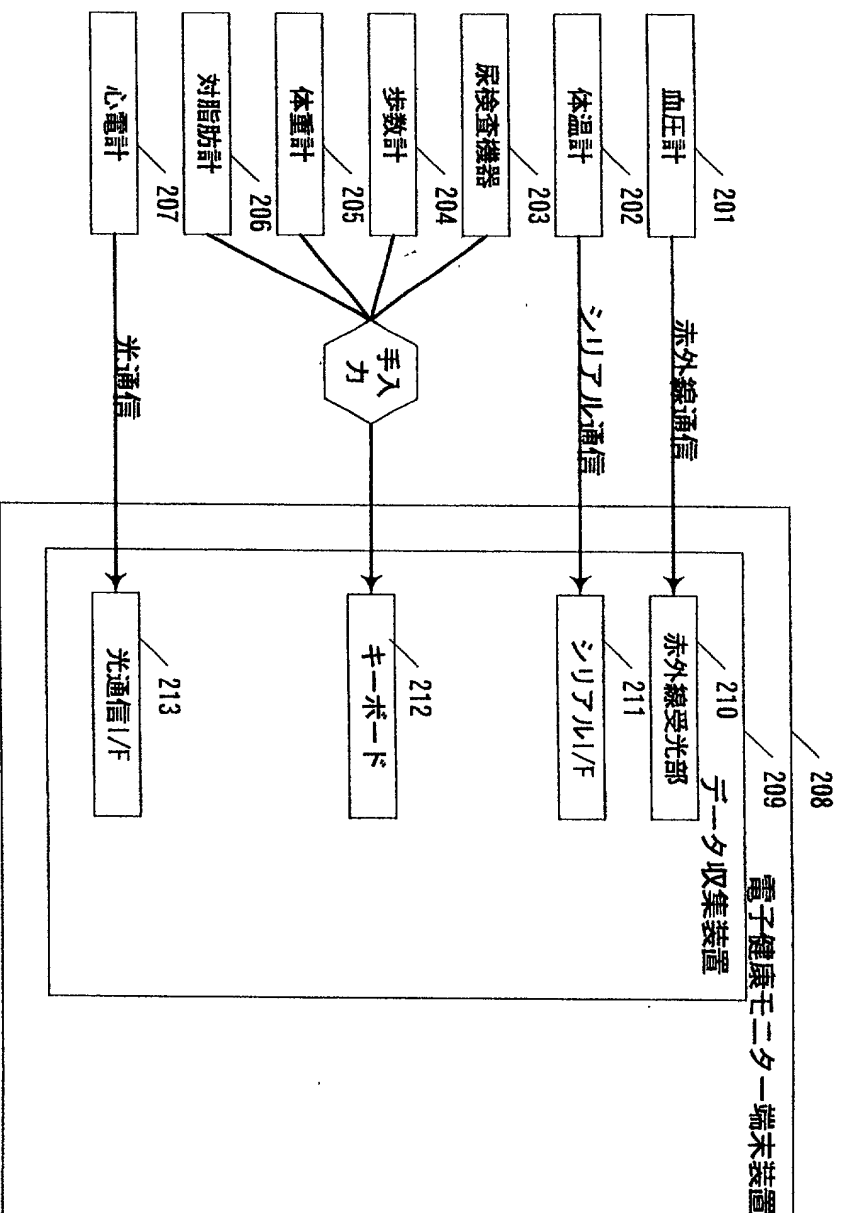
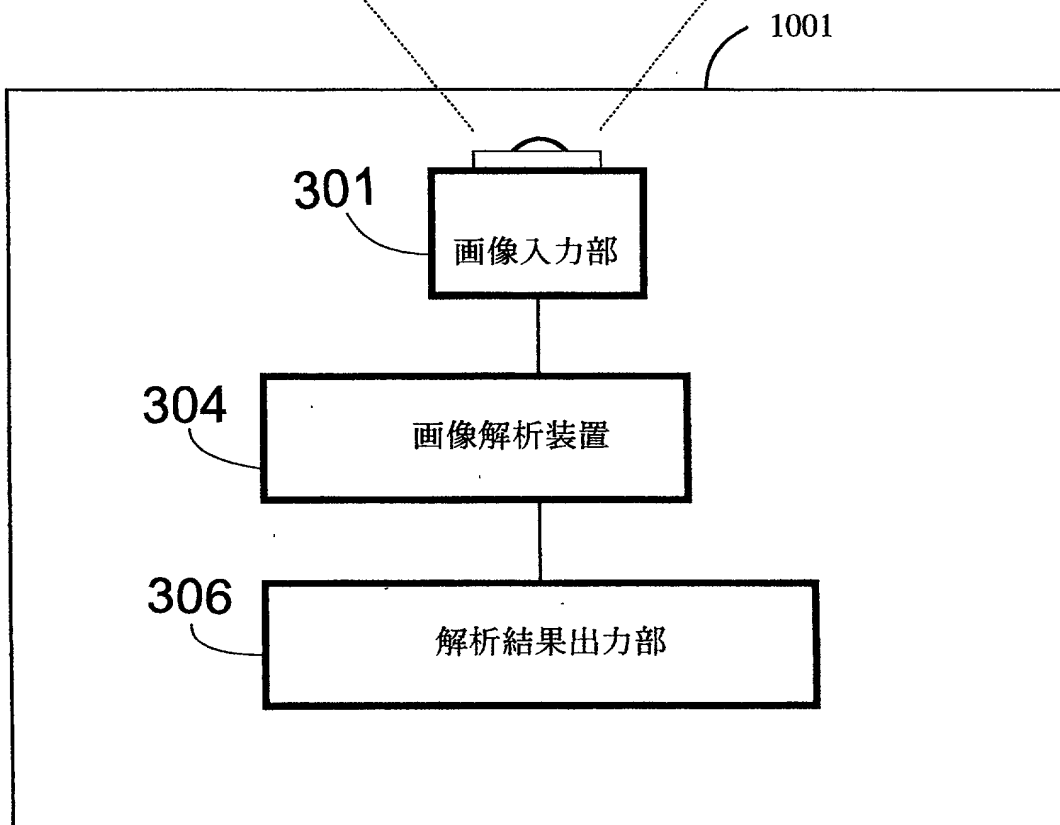
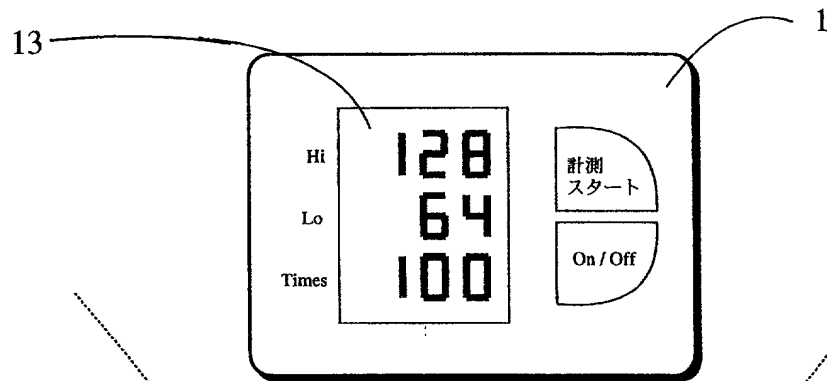


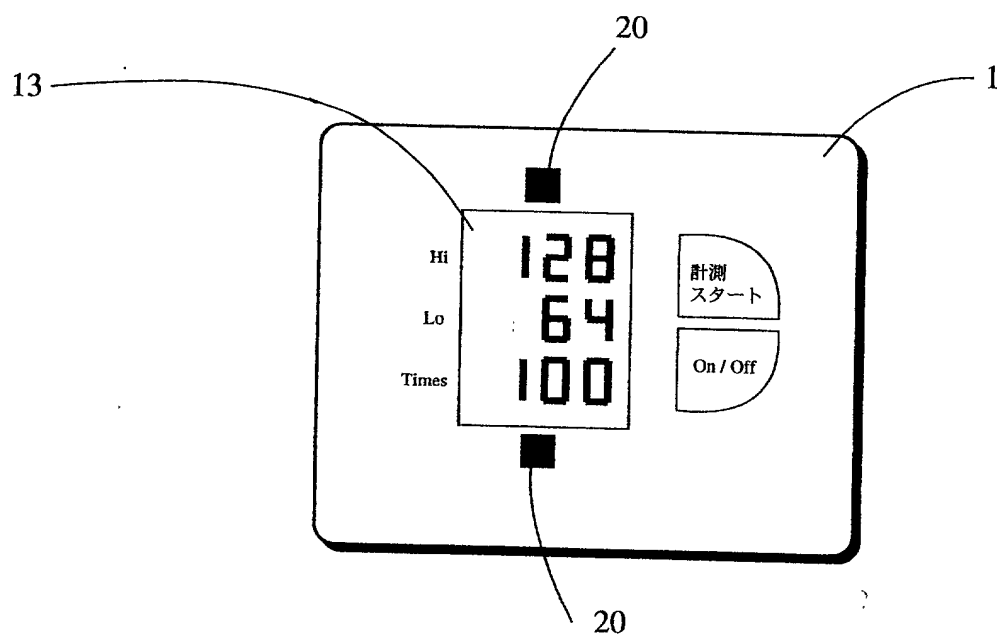
FIG. 17 PRIOR ART



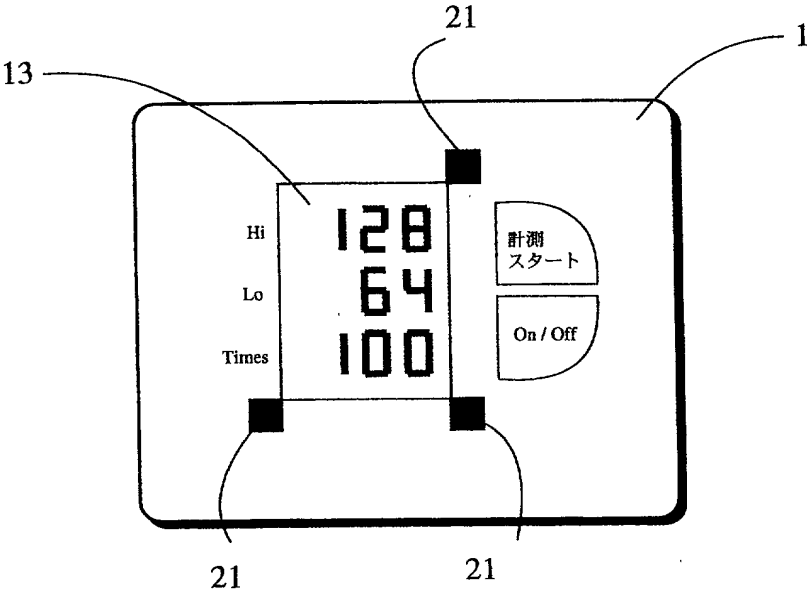
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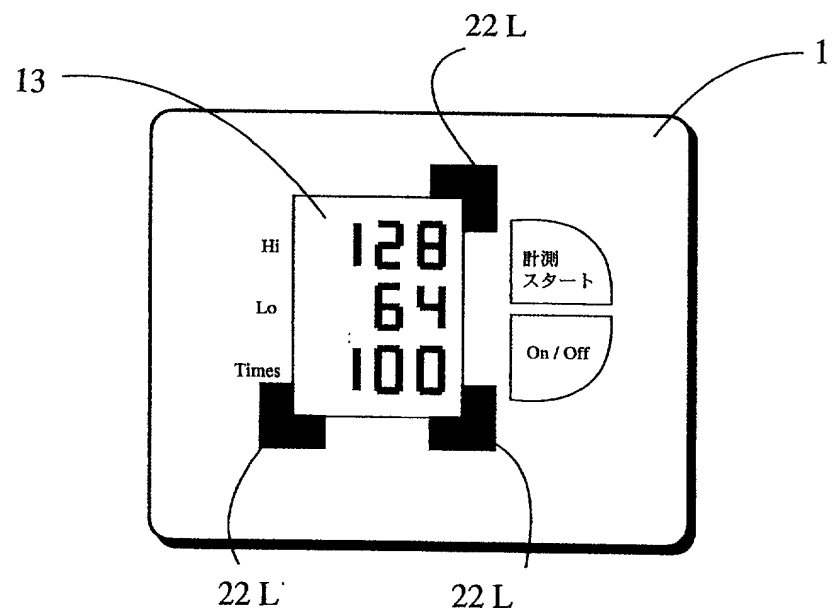
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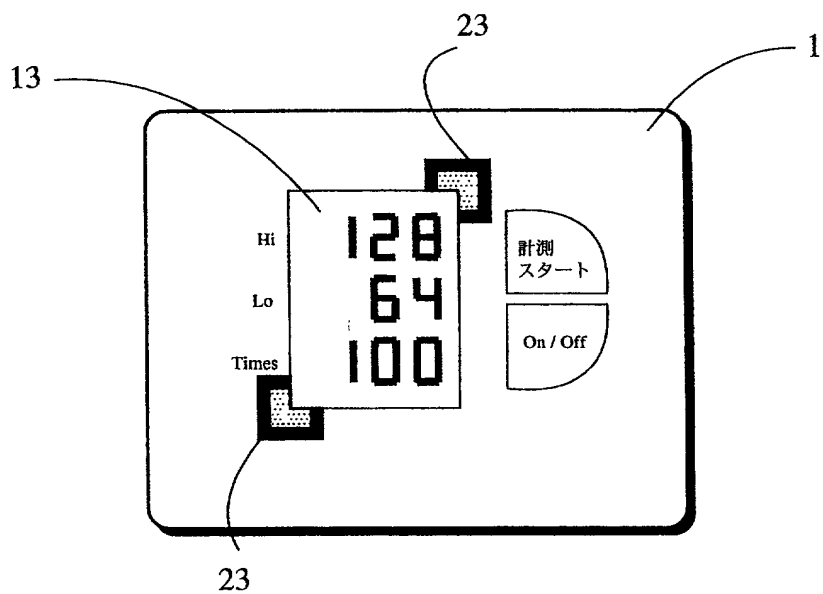
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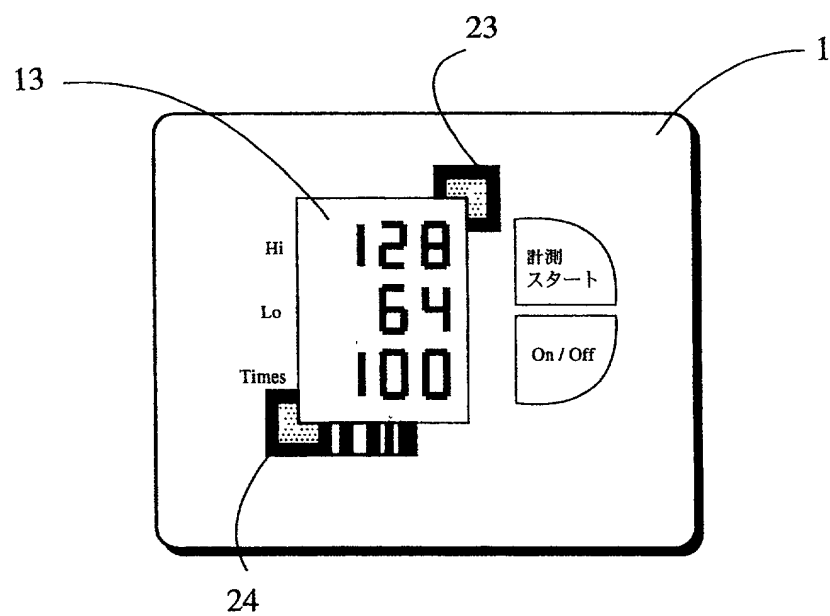
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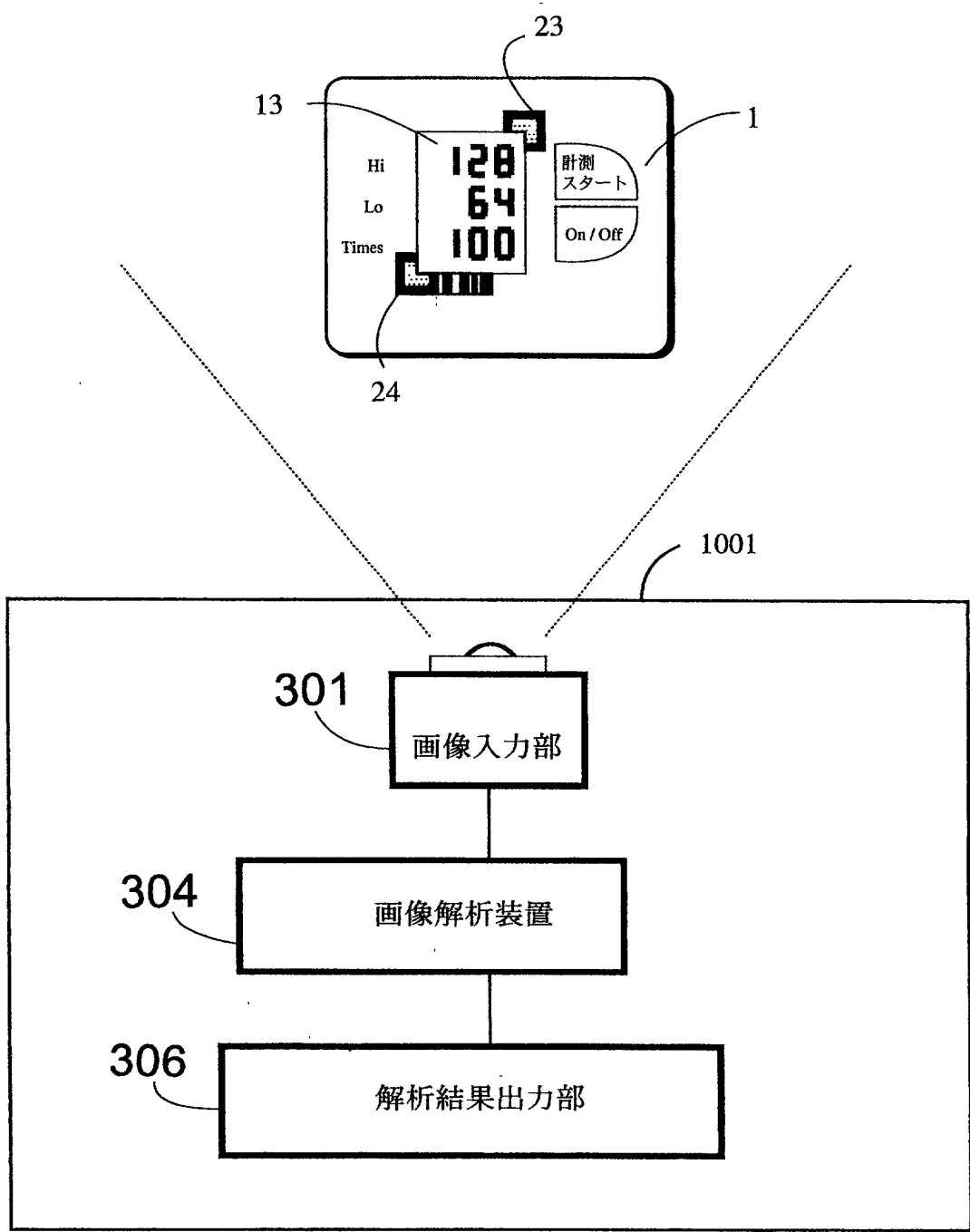
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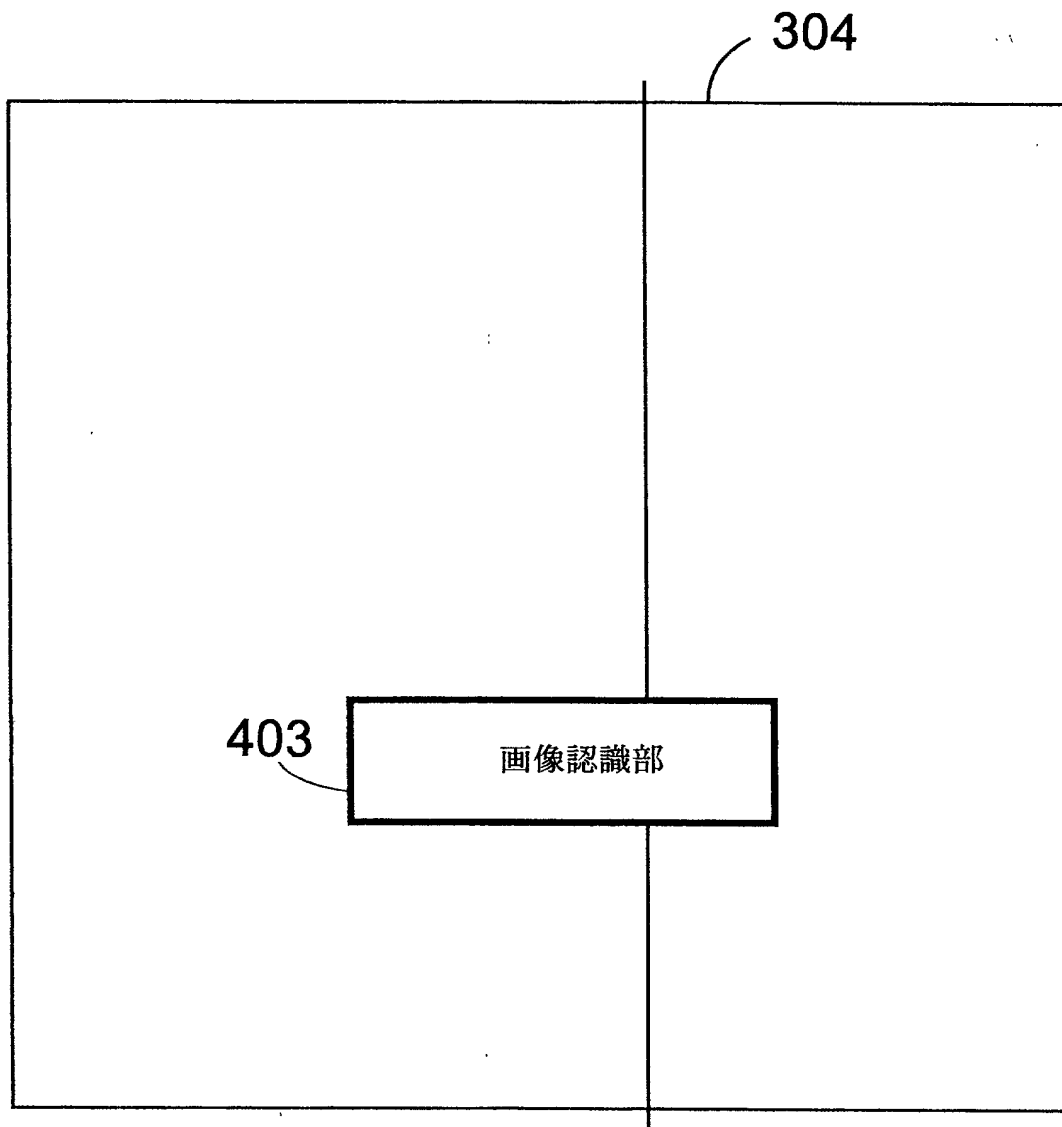
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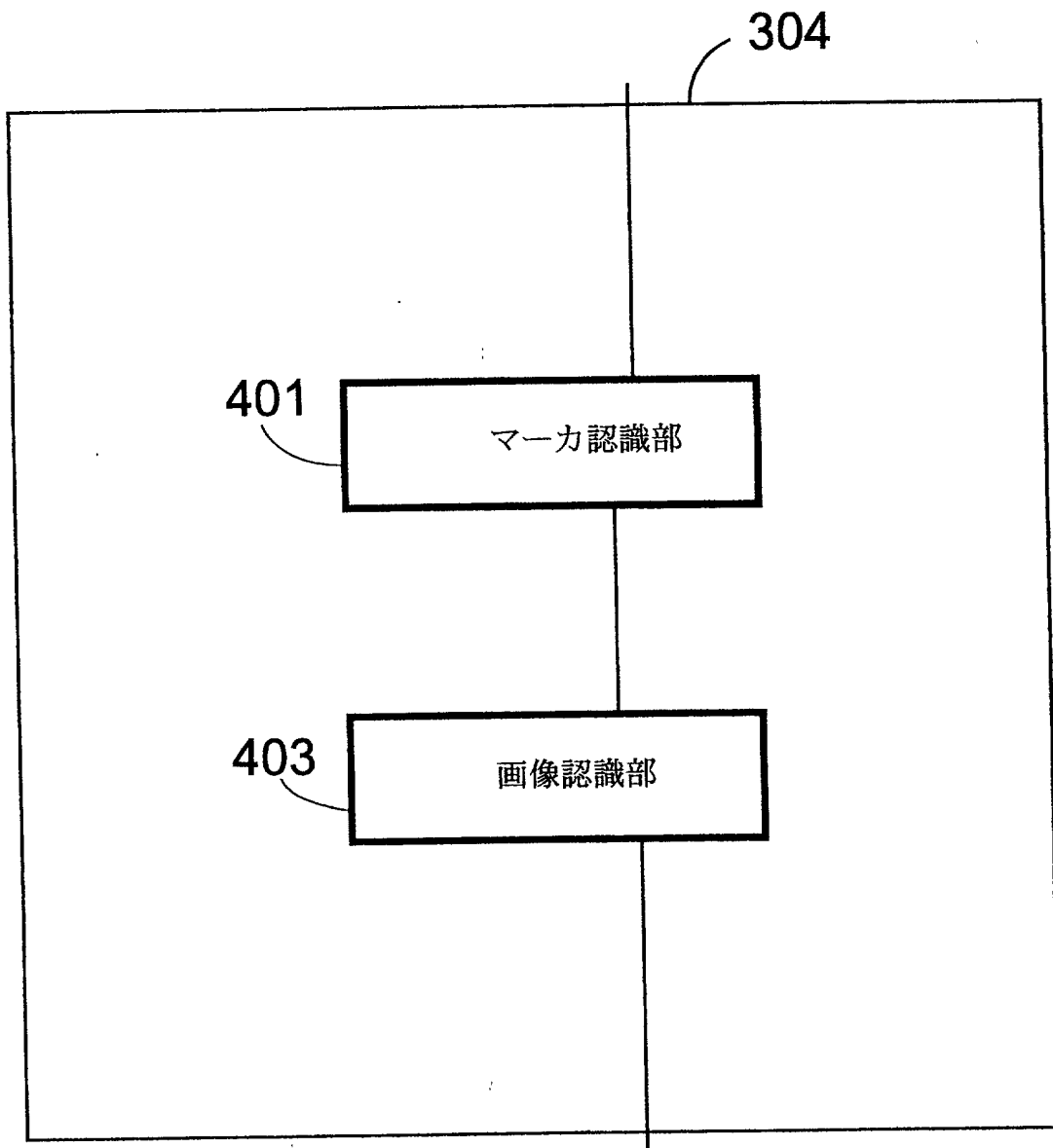
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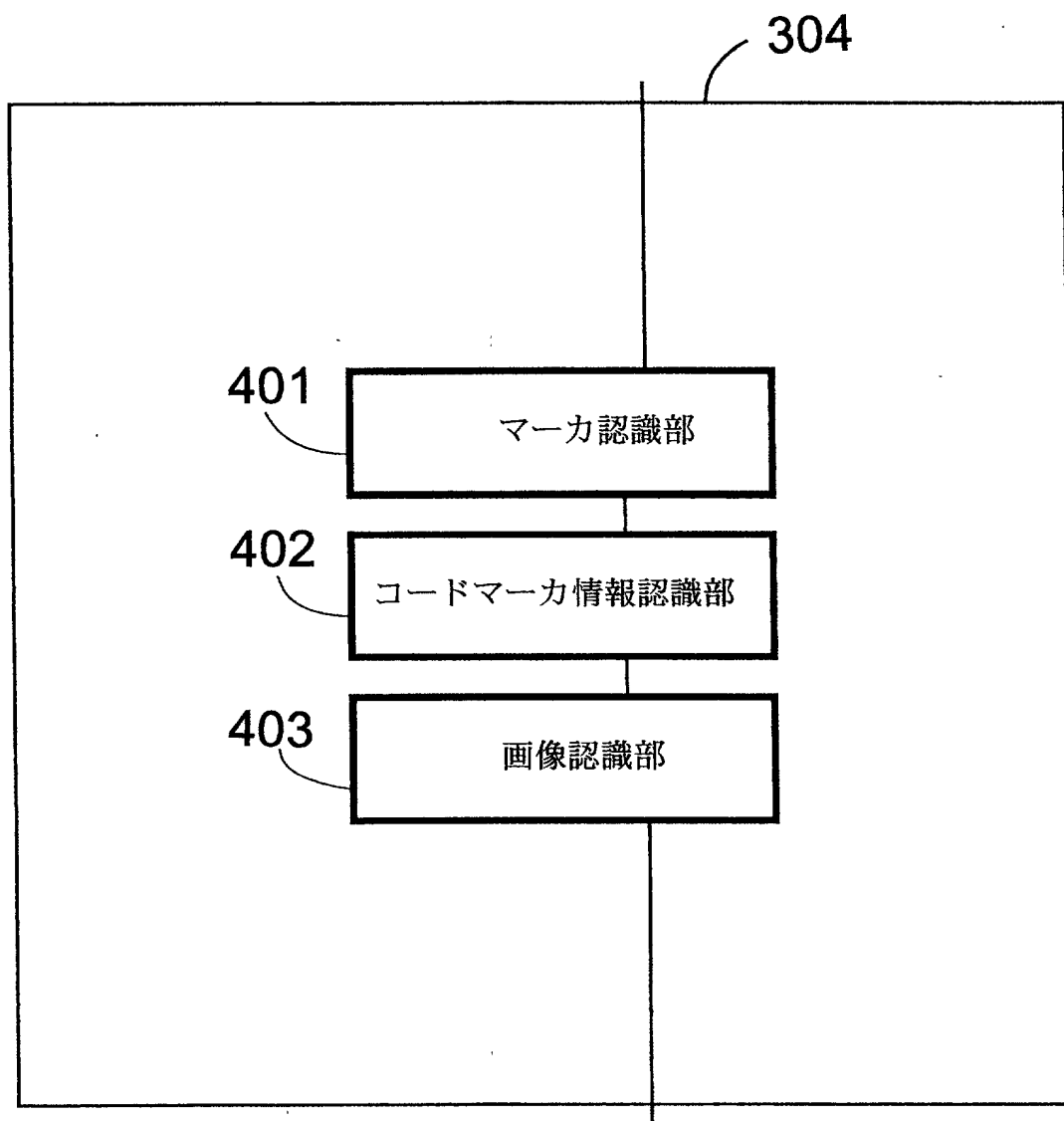
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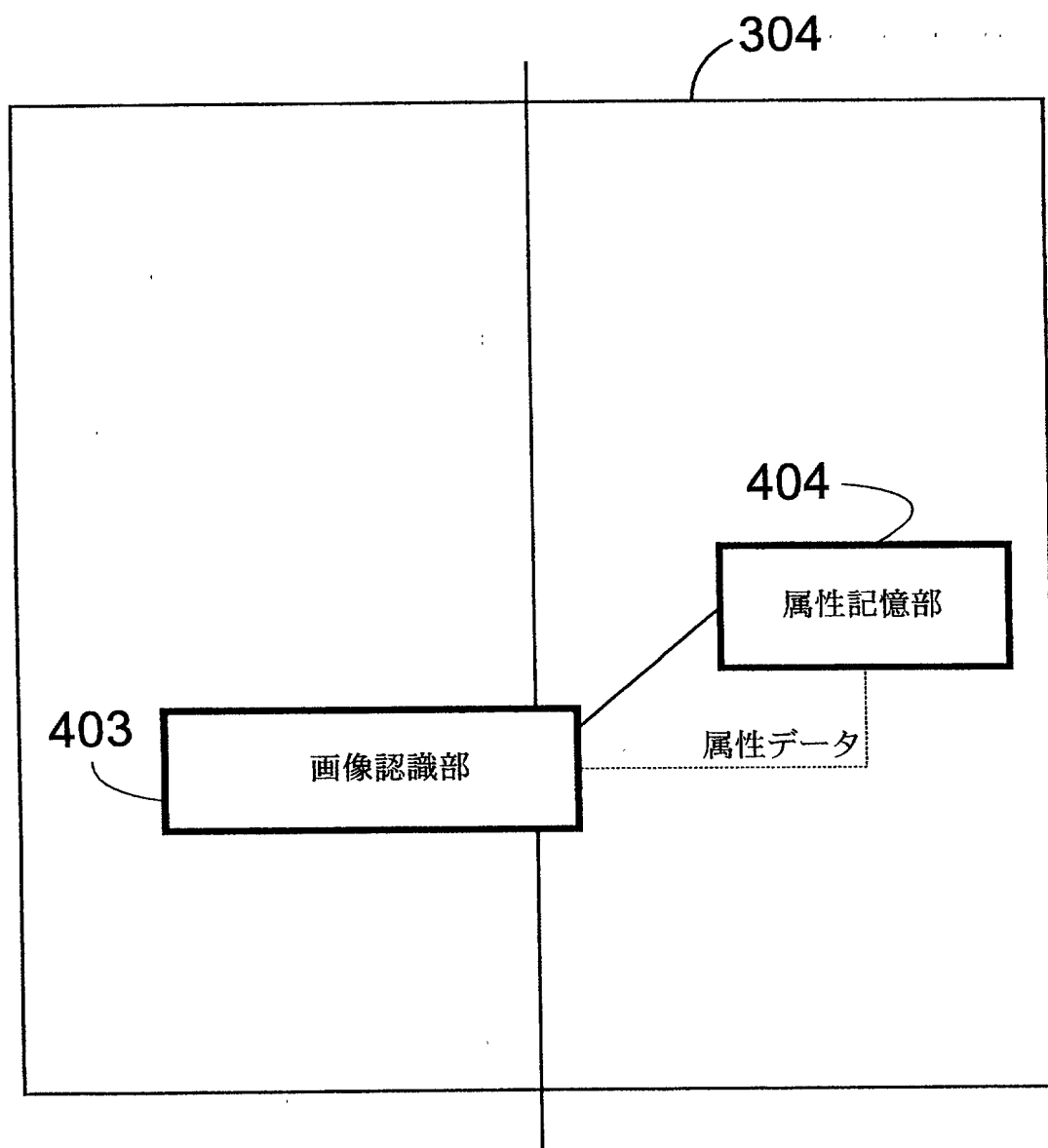
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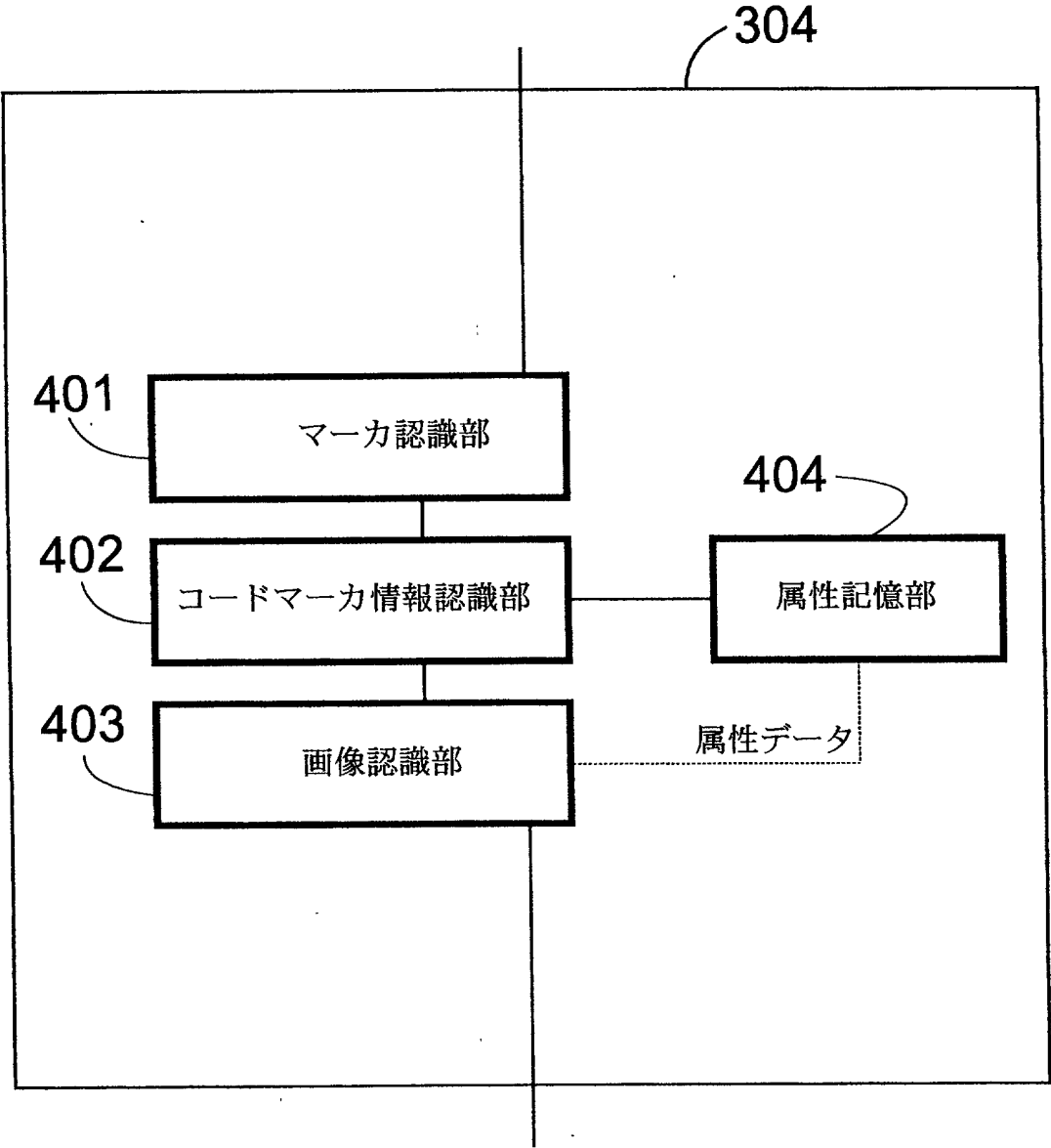
F i g . 2 7



F i g . 2 8

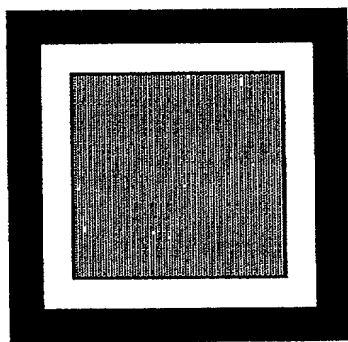


F i g . 2 9

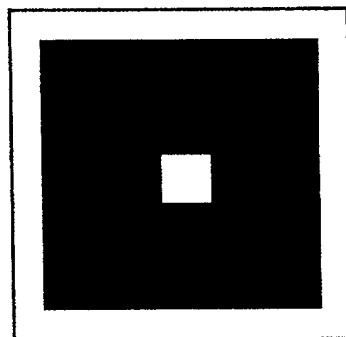


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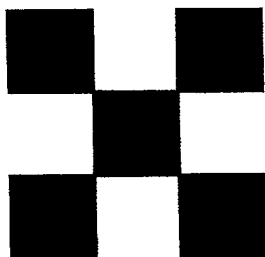
例 1



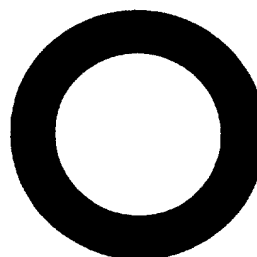
例 2



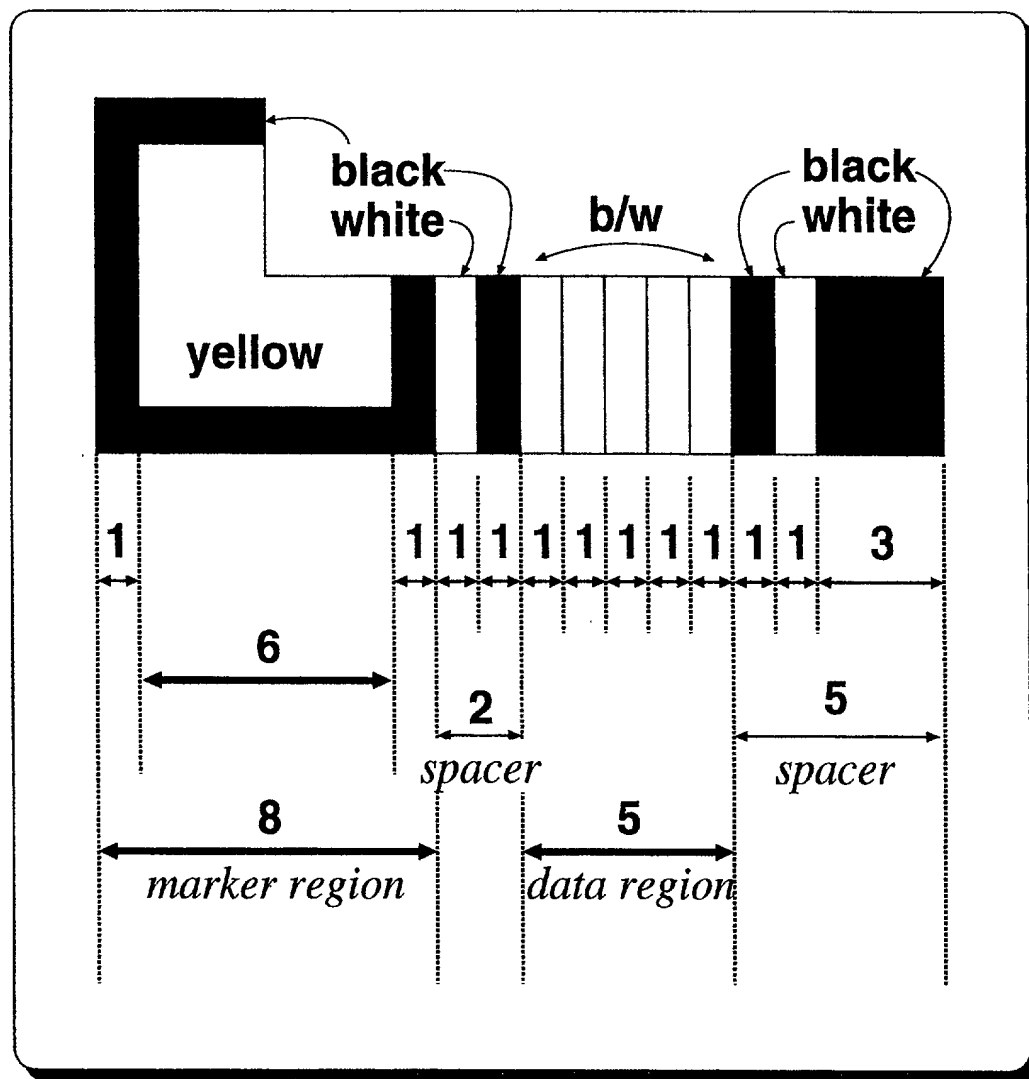
例 3



例 4



F i g . 3 1



F i g . 3 2

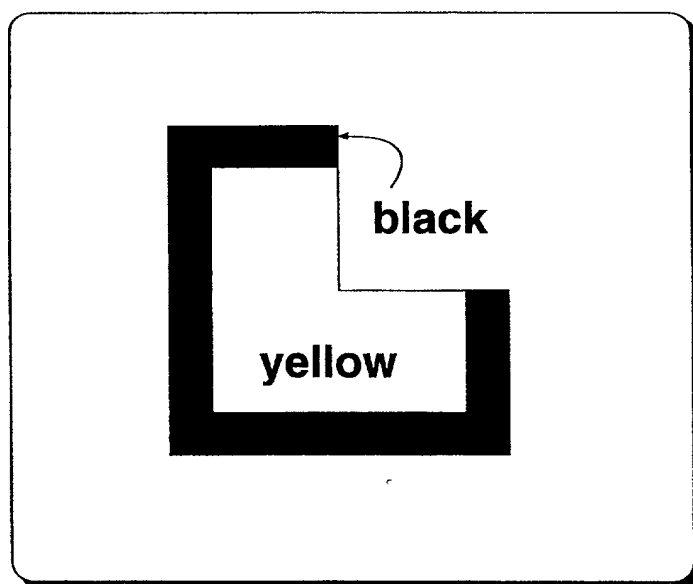
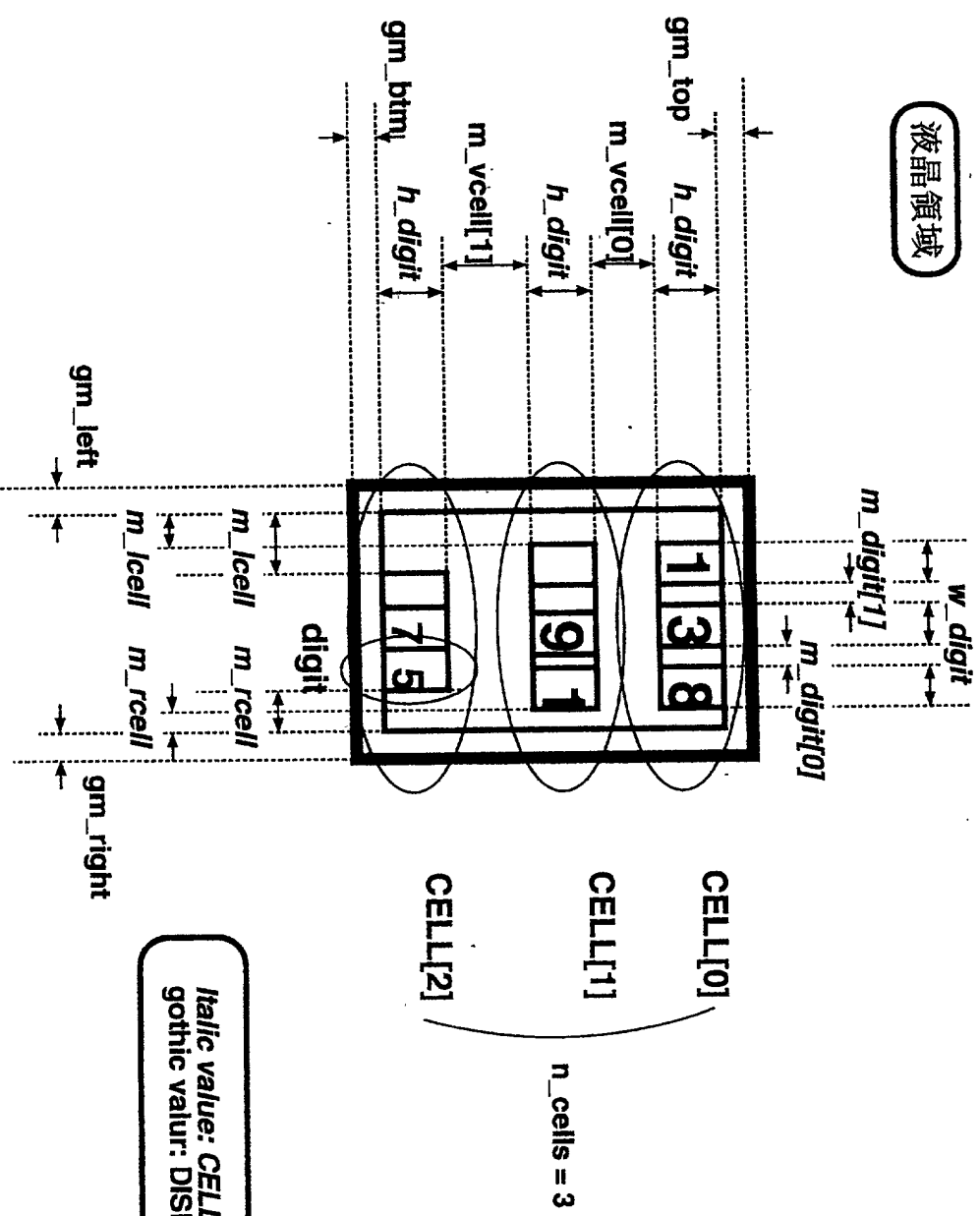


Fig. 3.3



italic value: CELL struct
gothic value: DISPLAY struct